

FIG.2

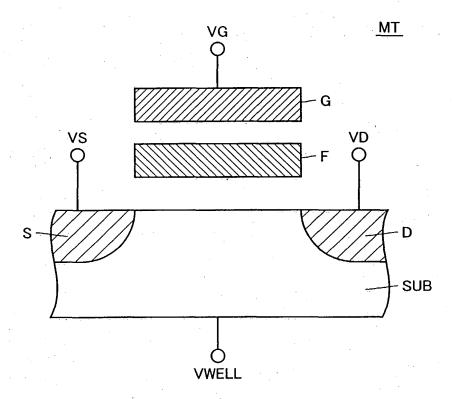


FIG.3

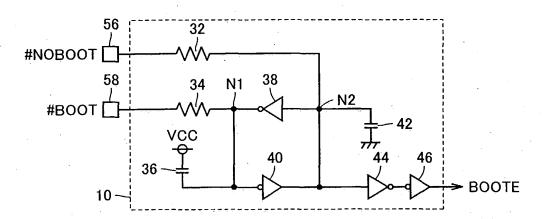


FIG.4

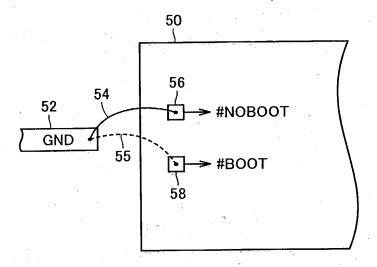
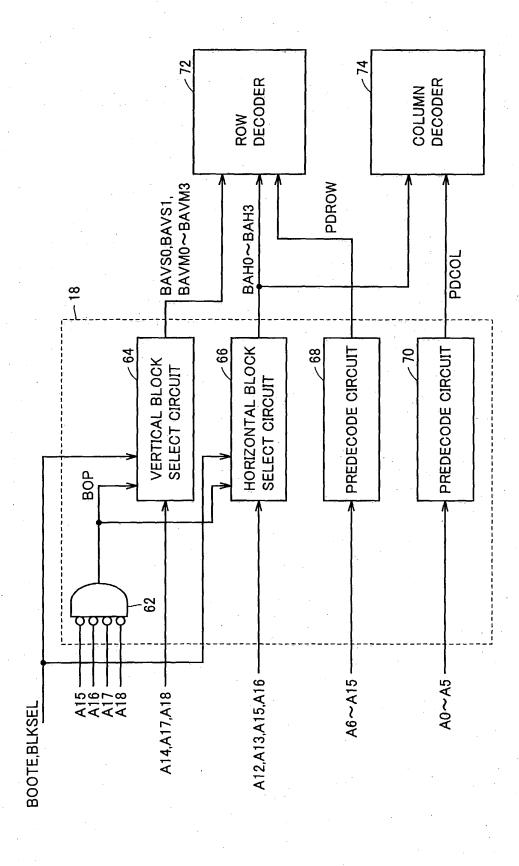
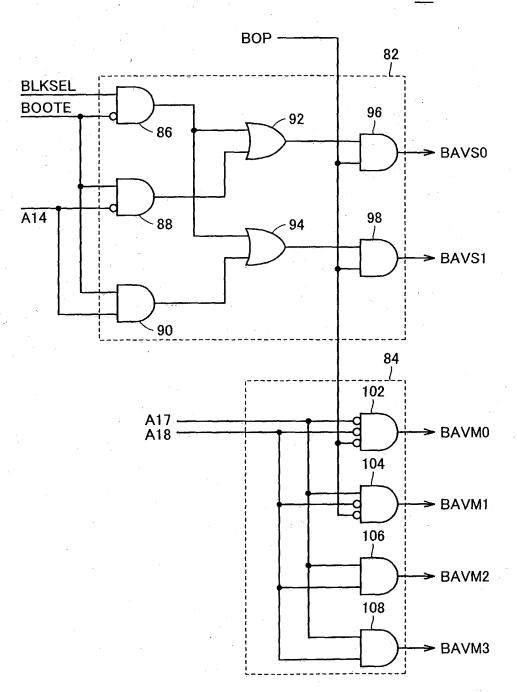


FIG.5

#NOBOOT	#BOOT	вооте
L	H or OPEN	L
H or OPEN	L	Н





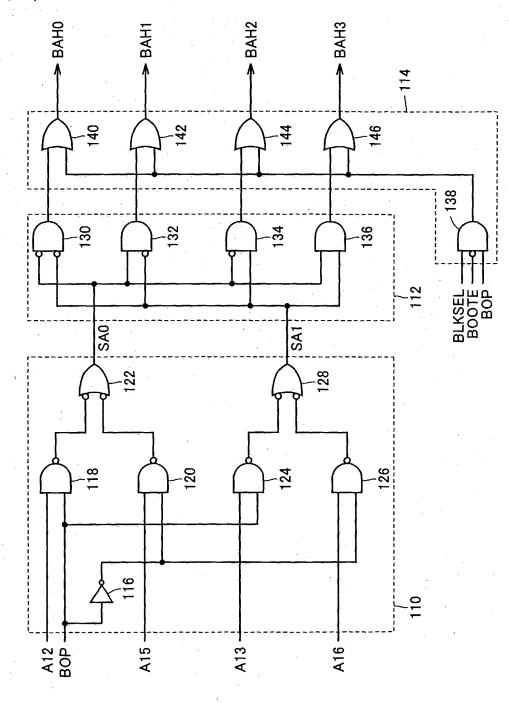
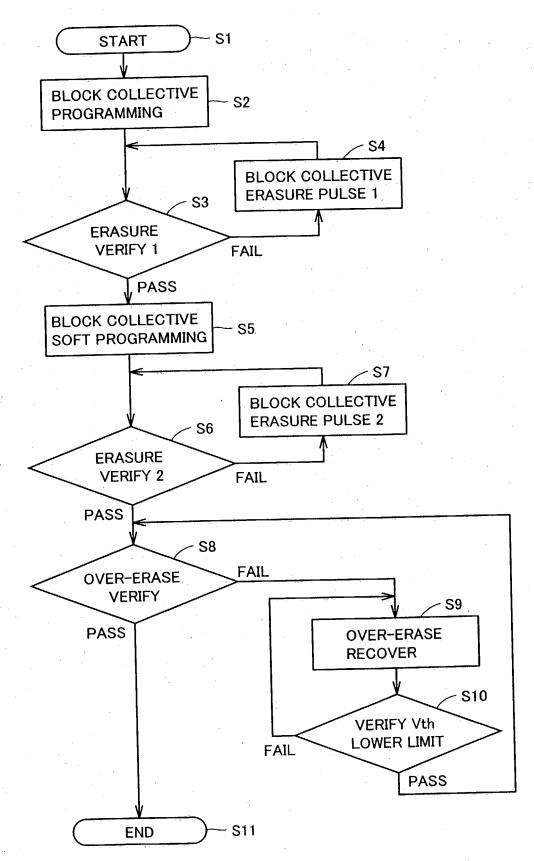


FIG.8

FIG.9



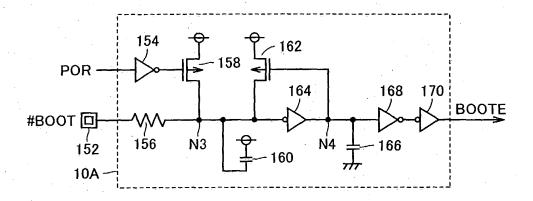


FIG.11

#BOOT	вооте
L	Н
H or OPEN	L

FIG.12

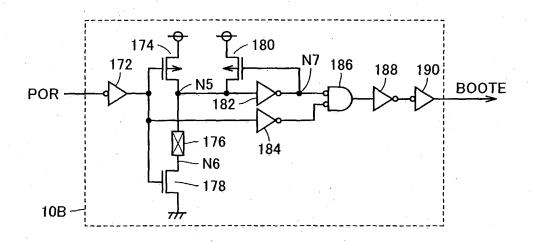


FIG.13

LT FUSE	вооте
BURN OUT	H
CONDUCT	L

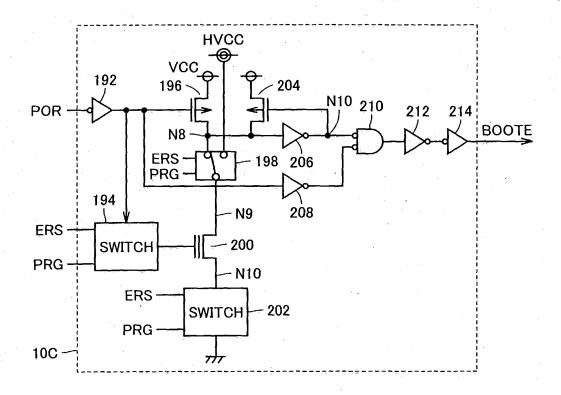


FIG.15

MEMORY TRANSISTOR Vth	вооте
HIGH	Н
LOW	L

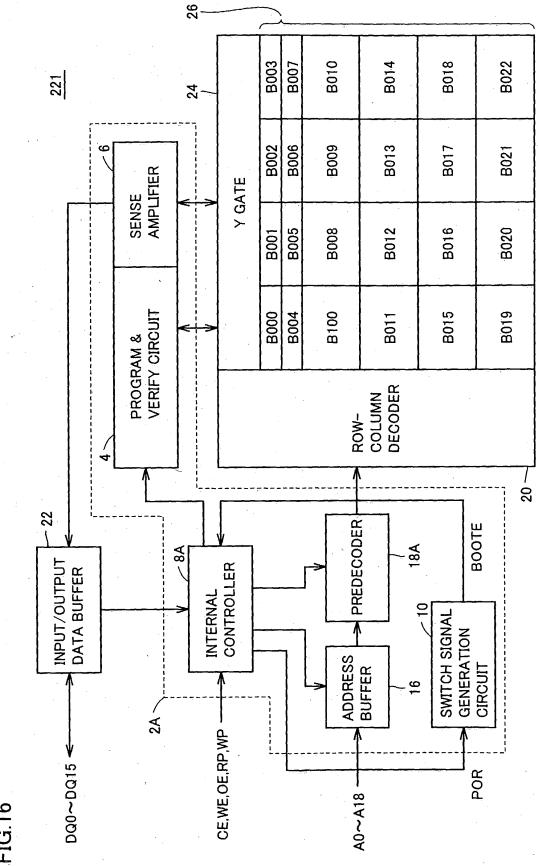


FIG.17

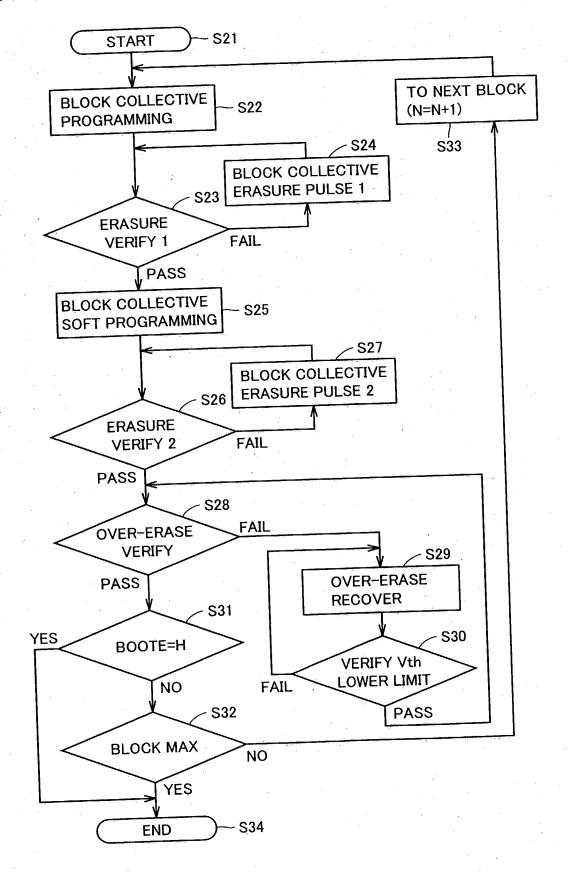


FIG.1

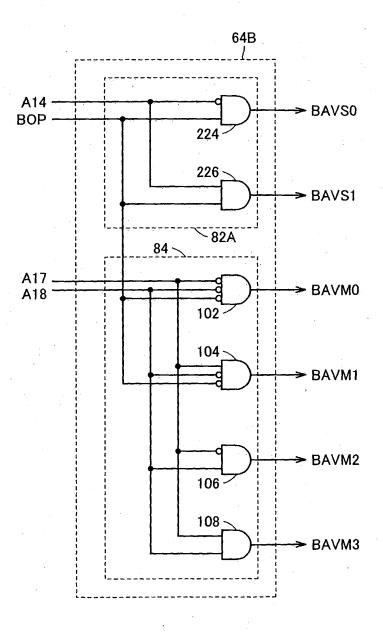
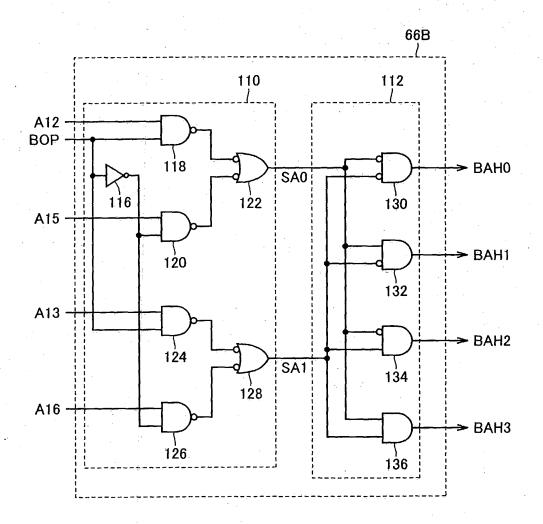


FIG.20



CORRESPONDING (	B000	B001	B002	B003
TO BOTTOM BOOT	B004	B005	B006	B007
TO BOLLOM POOL (	B100	B008	B009	B010
	B011	B012	B013	B014
	B015	B016	В017	B018
	B019	B020	B021	B200
CORRESPONDING (	B022	B023	B024	B025
TO TOP BOOT	B026	B027	B028	B029
1010, 500,				

		•		30
04				
7	B000	B001	B002	B003
	B004	B005	B006	B007
1	B100	B008	B009	B010
i ! ! ! ! !	B011	B012	B013	B014
f 1 1 1 1 1	B015	B016	B017	B018
1	B019	B020	B021	B022
06 —				
	B023	B024	B025	B026
; ; ; ; ; ;	B027	B028	B029	B030
! ! ! !	B031	B032	B033	B034
1	B035	B036	B037	B200
į	B038	B039	B040	B041
į	B042	B043	B044	B045

	•			308
310				
	B000	B001	B002	B003
	B004	B005	B006	B007
	B100	B008	B009	B010
	B011	B012	B013	B014
	B015	B016	B017	B018
 	B019	B020	B021	B022
312				
	B023	B024	B025	B026
	B027	B028	B029	B030
	B031	B032	B033	B034
1 1 1 1 1 1 1	B035	B036	B037	B038
ĺ				

				314
316	B000	B001	B002	B003
; 1 1 ; 1	B004	B005	B006	B007
{	B008	B009	B010	B011
           	B012	B013	B014	B015
318				
1	B016	B017	B018	B019
; ; ; ; ;	B020	B021	B022	B023
	B024	B025	B026	B027
	B028	B029	B030	B200
	B031	B032	B033	B034
	B035	B036	B037	B038

				320 /	)
322					
1	В000	B001	B002	В003	
	B004	B005	B006	B007	
	B008	B009	B010	B011	
	B012	B013	B014	B015	<i>.</i>
324					
	B016	B017	B018	B019	
 	B020	B021	B022	В023	
   1   1   1	B024	B025	B026	B027	
- J	B028	B029	B030	B031	
	<del></del>				

FIG.26 PRIOR ART

	вано	BAH1	BAH2	BAH3 500
BAVS0> [	B000	B001	B002	B003
BAVSI>	B004	B005	B006	B007
BAVM0>	B100	B008	B009	B010
BAVM1>	B011	B012	B013	B014
BAVM2>	B015	B016	B017	B018
BAVM3>	B019	В020	B021	B022

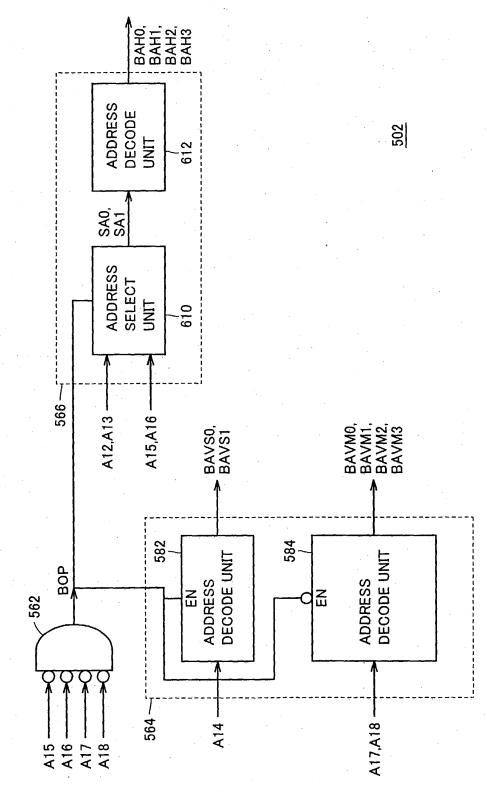


FIG.28 PRIOR ART

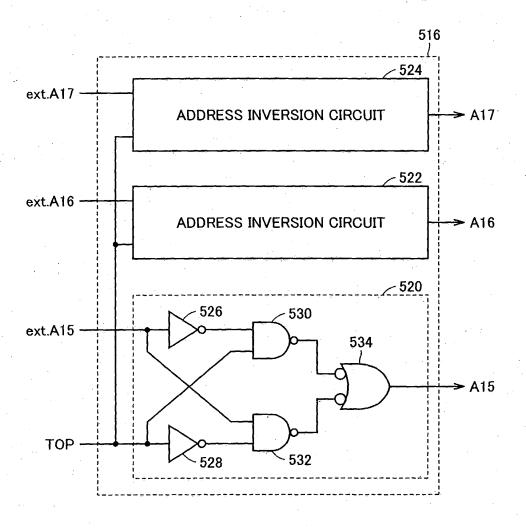


FIG.29 PRIOR ART

			700
В000	B001	B002	B003
B004	B005	B006	в007
B008	B009	B010	B011
B012	B013	B014	B015